

Cited Art

PATENT

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JC511 U.S. PATENT
09/318159
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Howard E. Rhodes
Serial No.: Divisional application of
U.S. Ser. No. 09/231,176
Filed :
Title : TRENCH ISOLATION FOR SEMICONDUCTOR DEVICES

Art Unit:
Examiner:

Box Patent Application

Assistant Commissioner for Patents
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the information listed on the attached form PTO 1449.

Under 35 U.S.C. §120, this application relies on the earlier filing date of application serial number 09/231,176, filed on January 14, 1999. The references listed on the attached PTO form 1449 were previously submitted to the Office in the prior application and, therefore, are not provided in this application.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

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Date of Deposit May 25, 1999
I hereby certify under 37 CFR 1.10 that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office To Addressee" with sufficient postage on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Valentia Figueroa
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Respectfully submitted,

Date: 5/25/99

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